

**74LVQ SERIES**

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
$V_{CC}$	Supply Voltage	2 to 3.6	V
$V_I$	Input Voltage	0 to $V_{CC}$	V
$T_A$	Operating Temperature (74 Series)	- 40 to 85	C

Type	Description	Package
74LVQ00	Quad 2-Input NAND Gate	TSSOP14/SO14
74LVQ02	Quad 2-Input NOR Gate	TSSOP14/SO14
74LVQ04	Hex Inverter	TSSOP14/SO14
74LVQ08	Quad 2-Input NAND Gate	TSSOP14/SO14
74LVQ125	Quad Bus Buffer (3-State)	TSSOP14/SO14
74LVQ138	3 to 8 Line Decoder (Inv.)	TSSOP16/SO16
74LVQ14	Hex Schmitt Inverter	TSSOP14/SO14
74LVQ157	Quad 2-Channel Multiplexer	TSSOP14/SO16
74LVQ174	Hex D-Type Flip-Flop With Clear	TSSOP14/SO16
74LVQ240	Octal Bus Buffer (3-State/Inv.)	TSSOP20/SO20
74LVQ241	Octal Bus Buffer (3-State)	TSSOP20/SO20
74LVQ244	Octal Bus Buffer (3-State)	TSSOP20/SO20
74LVQ245	Octal Bus Transceiver (3-State)	TSSOP20/SO20
74LVQ273	Octal D-Type Flip-Flop With Clear	TSSOP14/SO20
74LVQ32	Quad 2-Input OR Gate	TSSOP14/SO14
74LVQ373	Octal D-Type Latch (3-State)	TSSOP20/SO20
74LVQ374	Octal D-Type Flip-Flop (3-State)	TSSOP20/SO20
74LVQ573	Octal D-Type Latch (3-State)	TSSOP20/SO20
74LVQ574	Octal D-Type Flip-Flop (3-State)	TSSOP20/SO20
74LVQ74	Dual D-Type Flip-Flop With Preset And Clear	TSSOP14/SO14
74LVQ86	Quad Exclusive OR Gate	TSSOP14/SO14