# Overcoming Common Mode Range Issues When Using Harris Integrating Converters 

Authors: Paul Traynham and Juan C. Garcia

## Introduction

Some applications which utilize Harris' integrating analog to digital converters may require a way to overcome Common Mode Range (CMR) issues that may arise. Harris' integrating analog to digital converters such as those listed in Table 1 operate consistently with an input which is within the CMR and the full-scale voltage set by the user. As a result, the converter's full scale range may vary from 200 mV to as much as 2 V .

TABLE 1. HARRIS INTEGRATING CONVERTERS

| ICL7106 | $3^{1 / 1 / 2}$ Digit LCD Display ADC |
| :--- | :--- |
| ICL7107 | $3^{1 / 2}$ Digit LED Display ADC |
| ICL7116 | $3^{1 / 2}$ Digit LSD Display ADC with Display Hold |
| ICL7117 | $3^{1 / 2}$ Digit LED Display ADC with Display Hold |
| ICL7129 | $4^{1 / 2}$ Digit LCD Single Chip ADC |
| ICL7136 | $3^{11 / 2}$ Digit LCD Low Power Display ADC with <br> Overrange Recovery |
| ICL7137 | $3^{1 / 2}$ Digit LED Low Power Display ADC with <br> Overrange Recovery |
| HI7131 | $3^{1 / 1 / 2}$ Digit Low Power LCD Display ADC |
| HI7133 | $3^{1 / 2}$ Digit Low Power LED Display ADC |

## Applications

Figure 1 illustrates a typical application for the ICL7107, 31/2 Digit LED Display ADC, using a single 5V supply and full scale range set to 200 mV . Here, the IN LO input is tied to analog COMMON, and the differential input is measured with respect to it. Analog COMMON sets a voltage which is approximately 2.8V less than the positive supply, $\mathrm{V}_{+}$, when $\mathrm{V}+$ to $\mathrm{V}-\geq 6.8 \mathrm{~V}$. Here, $\mathrm{V}+$ to $\mathrm{V}-=5 \mathrm{~V}$, so a fixed 1.2 V reference is used. The CMR is defined as: $(\mathrm{V}-+1.0 \mathrm{~V})<\mathrm{V}_{\mathrm{IN}}<(\mathrm{V}+-0.5 \mathrm{~V})$. Not only does the input need to be within the common mode range, but also the full-scale range as well.
When operating these converters from a single supply and with an input voltage referenced to ground instead of COMMON, the designer can quickly run out of common mode range with which to work. In Figure 1, $\mathrm{V}_{+}=5 \mathrm{~V}$ and $\mathrm{V}-=0 \mathrm{~V} . \mathrm{V}_{\mathrm{IN}}$ is limited to a common mode range of +1 V to +4.5 V , but has a full scale of 200 mV . The 200 mV full scale is below the common mode range of the device. Even if fullscale is adjusted to 2 V , the full scale range of the device is limited to a 1 V swing ranging from 1 to 2 V .


FIGURE 1. ICL7107 OPERATED FROM A SINGLE +5V SUPPLY

One solution is to separate COMMON and IN LO and tie IN LO to ground. The problem here is that gross errors can occur if IN LO is pulled to far from COMMON. A much better solution is to use a simple, non-inverting amplifier adder circuit as seen in Figure 2. A Harris CA3130 operational amplifier was chosen because it operates from a single 5 V supply, has rail-to-rail output swing and an input range that includes ground.


FIGURE 2. SIMPLE SUMMING AMP TO RESOLVE CMR PROBLEM By analyzing the circuit in Figure 2, we see that:
$V_{\text {OUT }}=\left(1+\frac{R_{f}}{R}\right) \times V_{I N}$,
where
$\mathrm{V}_{\mathrm{IN}}{ }^{\prime}=\frac{1}{2}\left(\mathrm{~V}_{\mathrm{IN}}+\mathrm{V}_{\mathrm{COM}}\right)$
to define the operational amplifier output.
Substituting equation 2 into equation 1 , and setting $R_{f}=R$ yields:
$\mathrm{V}_{\text {OUT }}=2\left(\frac{1}{2}\left(\mathrm{~V}_{\text {IN }}+\mathrm{V}_{\text {COM }}\right)\right)=\mathrm{V}_{\text {IN }}+\mathrm{V}_{\text {COM }}$
Using this circuit, the operational amplifier's output signal ( $\mathrm{V}_{\text {OUT }}$ ) is now the sum of the incoming signal $\left(\mathrm{V}_{\mathrm{IN}}\right)$ and COMMON. In other words, $\mathrm{V}_{\mathrm{IN}}$ is now referenced with respect to COMMON and not ground. With a single 5 V supply, an external voltage reference must be used because the internal reference is only active when $\mathrm{V}+$ to $\mathrm{V}-\geq 6.8 \mathrm{~V}$, and $\mathrm{V}_{\mathrm{COM}}=\mathrm{COMMON} \cong \mathrm{V}+-1.2 \mathrm{~V}=3.8 \mathrm{~V}$. The signal input to the ICL7107 will now be offset by 3.8 V , which is well within the common mode range of the device. Even if $\mathrm{V}+$ changes, as in battery applications, COMMON will keep the signal within the common mode range of the converter.

## Measuring the Supply Rail

In battery applications where a designer wishes to not only power the converter from a single supply, but also monitor and display the battery voltage, common mode range issues again arise. From the CMR equation we note that:

$$
1 \mathrm{~V}<\mathrm{V}_{\mathrm{IN}}<\left(\mathrm{V}_{+}-0.5 \mathrm{~V}\right)
$$

At first glance, it may seem that an easy solution would be to use a simple resistor voltage divider and reduce $\mathrm{V}_{\mathrm{IN}}$ by a factor of 10, set full-scale to 2 V and supply this to the ADC. For common battery voltages of 6 V to 12 V , the corresponding input voltages would be 0.6 to 1.2 V . However, if we go back to the above common mode range equation, it states that $\mathrm{V}_{\mathrm{IN}}$ must be greater than 1 V . Figure 3 illustrates another operational amplifier configuration to solve this problem. Again a CA3130 was chosen because of its characteristics.


FIGURE 3. SIMPLE OP AMP CIRCUIT TO MEASURE SUPPLY RAIL (V+)

Here an ICL7106, $3^{1 / 2}$ 2 Digit LCD Display ADC with full scale set to 2 V is shown. In this circuit, the operational amplifier is set in an inverting gain configuration, because gains of 0.1 are desirable, and because we are only concerned with positive input voltages.

Again analyzing the operational amplifier configuration, we note that:

$$
\begin{equation*}
\frac{\left(\mathrm{V}_{\mathrm{IN}}{ }^{\prime}-\mathrm{V}_{\mathrm{IN}}\right)}{\mathrm{R}}=\frac{\left(\mathrm{V}_{\mathrm{OUT}}-\mathrm{V}_{\mathrm{IN}}{ }^{\prime}\right)}{\mathrm{R}_{\mathrm{F}}} \tag{EQ.4}
\end{equation*}
$$

and that:
$V_{I N}{ }^{\prime}=V_{C O M}\left(\frac{R_{2}}{R_{1}+R_{2}}\right)$
Setting $R=10 R_{F}, R_{F}=R_{1}$ and $R_{2}=R_{1}$, the equations can be reduced to:
$V_{I N}{ }^{\prime}-V_{I N}=10\left(V_{O U T}-V_{I N}\right)$
(EQ. 6)
and
$\mathrm{V}_{\mathrm{IN}}{ }^{\prime}=\left(\frac{\left(10 \times \mathrm{V}_{\mathrm{COM}} \times \mathrm{R}_{1}\right)}{11 \mathrm{R}_{1}}\right)=\frac{10 \mathrm{~V}_{\mathrm{COM}}}{11}$
Combining equations 6 and 7 reveals that:
$\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {COM }}-\frac{\mathrm{V}_{\text {IN }}}{10}$
The output of the operational amplifier ( $\mathrm{V}_{\text {OUT }}$ ), which is the input of the $A D C$, is now $-0.1 \mathrm{~V}_{\mathrm{IN}}$ with respect to COMMON, and since $\mathrm{V}_{I N}=\mathrm{V}_{+}$, the ADC will measure $-10 \%$ of the supply. Simply driving the proper decimal point and not driving the sign indicator will display the proper voltage. For example, a battery voltage of 11.6 V will be measured as -1.16 V and can easily be displayed as 11.60 V on a $3 \frac{1}{2}$ Digit Display.

For more information on Harris integrating analog to digital converters, please refer to the AnswerFAX document numbers outlined in Table 2.

TABLE 2. AnswerFAX INFORMATION

| DEVICE | DESCRIPTION | AnswerFAX DOCUMENT NUMBER |
| :---: | :---: | :---: |
| ICL7106 | $31 / 2$ Digit LCD/LED Display ADC | 3082 |
| ICL7107 | $31 / 2$ Digit LCD/LED Display ADC | 3082 |
| ICL7116 | 3½ Digit LSD/LED Display ADC with Display Hold | 3083 |
| ICL7117 | 3½ Digit LSD/LED Display ADC with Display Hold | 3083 |
| ICL7129 | 4½ Digit LCD Single Chip ADC | 3085 |
| ICL7136 | 3½ Digit LCD/LED Low Power Display ADC with Overrange Recovery | 3086 |
| ICL7137 | 3½ Digit LCD/LED Low Power Display ADC with Overrange Recovery | 3086 |

All Harris Semiconductor products are manufactured, assembled and tested under ISO9000 quality systems certification.

Harris Semiconductor products are sold by description only. Harris Semiconductor reserves the right to make changes in circuit design and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Harris is believed to be accurate and reliable. However, no responsibility is assumed by Harris or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Harris or its subsidiaries.

## Sales Office Headquarters

For general information regarding Harris Semiconductor and its products, call 1-800-4-HARRIS

NORTH AMERICA
Harris Semiconductor
P. O. Box 883, Mail Stop 53-210

Melbourne, FL 32902
TEL: 1-800-442-7747
(407) 729-4984

FAX: (407) 729-5321

EUROPE
Harris Semiconductor Mercure Center
100, Rue de la Fusee
1130 Brussels, Belgium
TEL: (32) 2.724.2111
FAX: (32) 2.724.22.05

ASIA
Harris Semiconductor PTE Ltd.
No. 1 Tannery Road
Cencon 1, \#09-01
Singapore 1334
TEL: (65) 748-4200
FAX: (65) 748-0400

SEMICONDUCTOR
SEMICONDUCTOR

